

Daisy



File: ES-Seed3-DevKit-Pedal-Rev1_1.kicad_sch

Peripherals



File: ES-Seed3-DevKit-Pedal-Rev1_2.kicad_sch

Audio IO



File: ES-Seed3-DevKit-Pedal-Rev1_3.kicad_sch

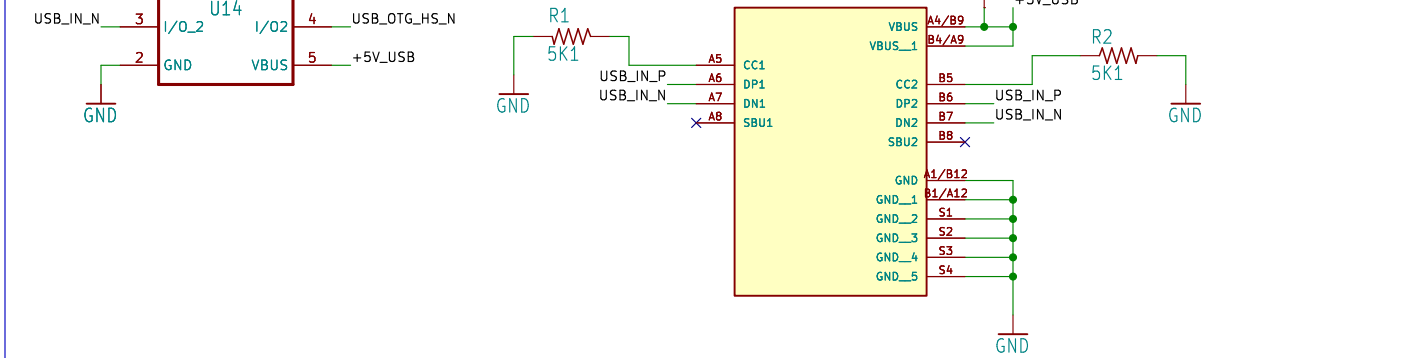
Power



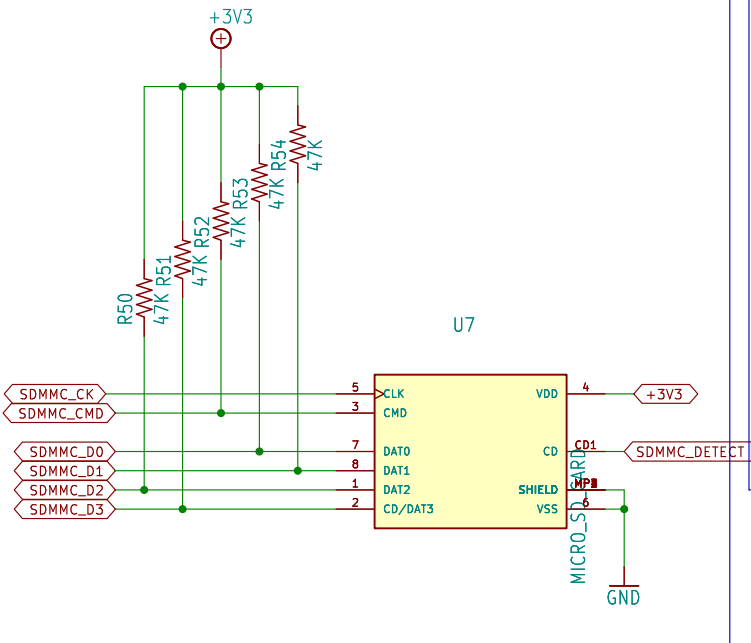
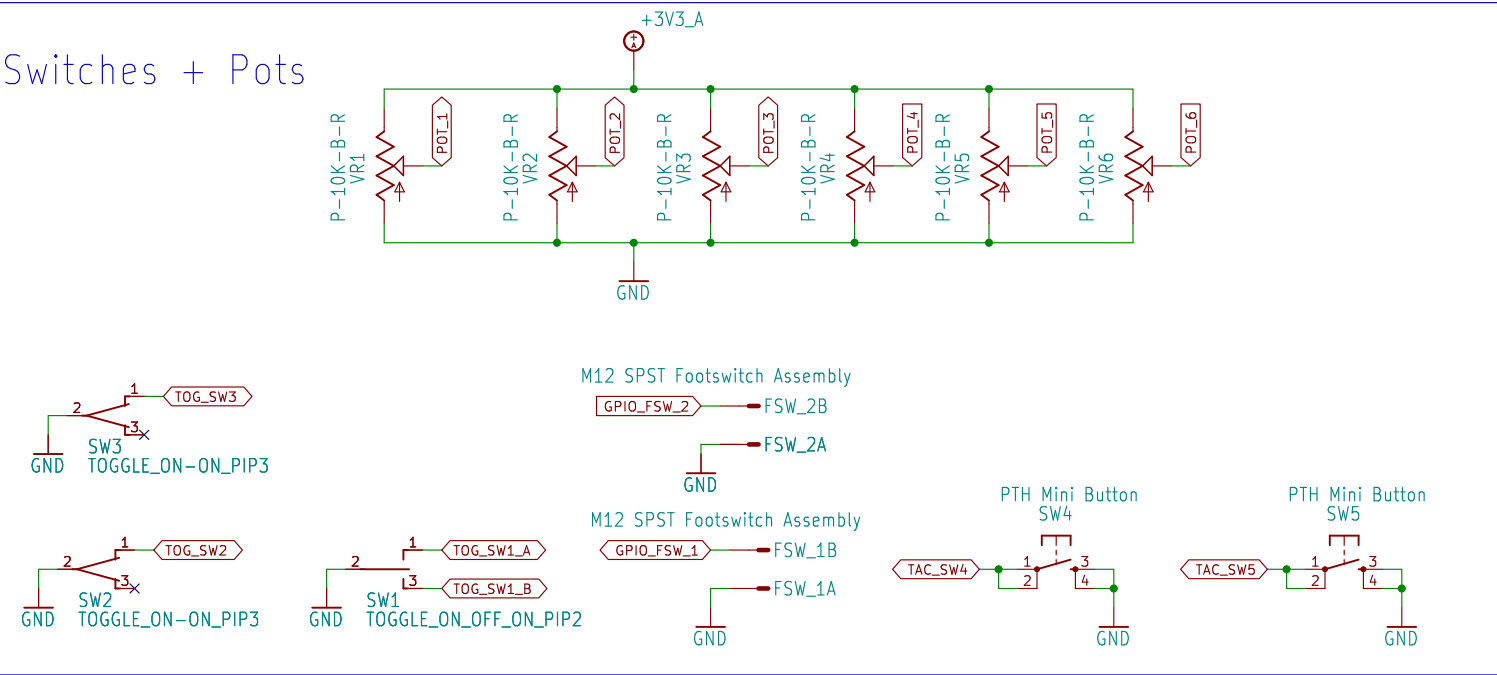
File: ES-Seed3-DevKit-Pedal-Rev1_4.kicad_sch

- ✖  H1
MOUNT-PAD-ROUND3.5
- ✖  H2
MOUNT-PAD-ROUND3.5
- ✖  H3
MOUNT-PAD-ROUND3.5
- ✖  H4
MOUNT-PAD-ROUND3.5

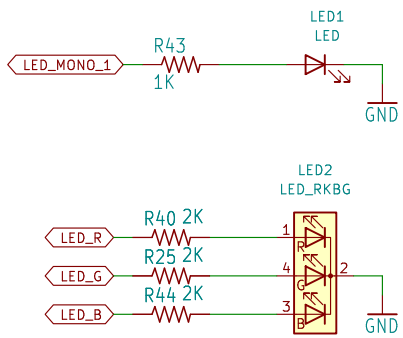
USB_IN_P 1 I/O 1 I/O 6 USB_OTG_HS_P J7 USB4110GEA PWR_FLAG



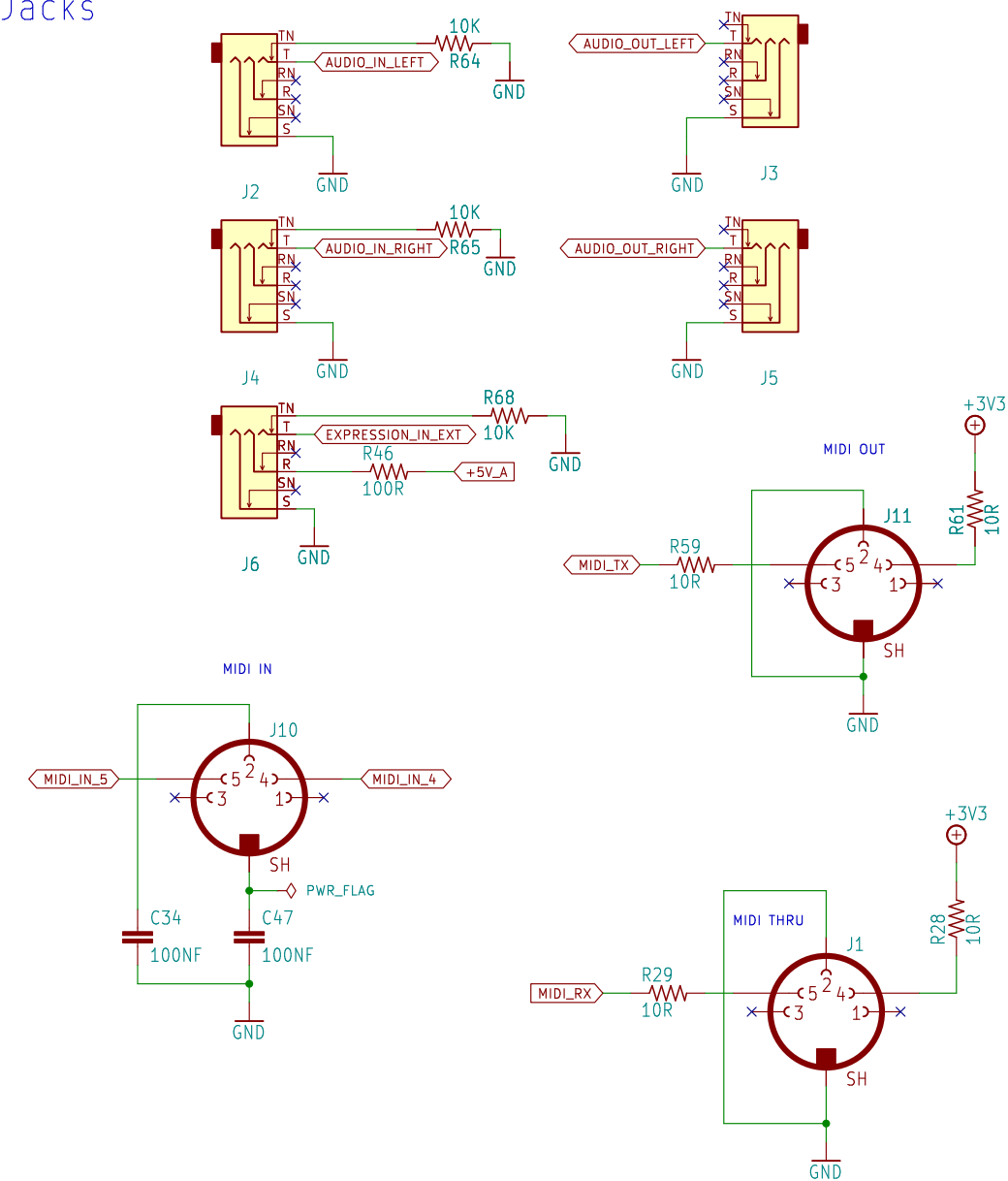
Switches + Pots



LEDs



Jacks

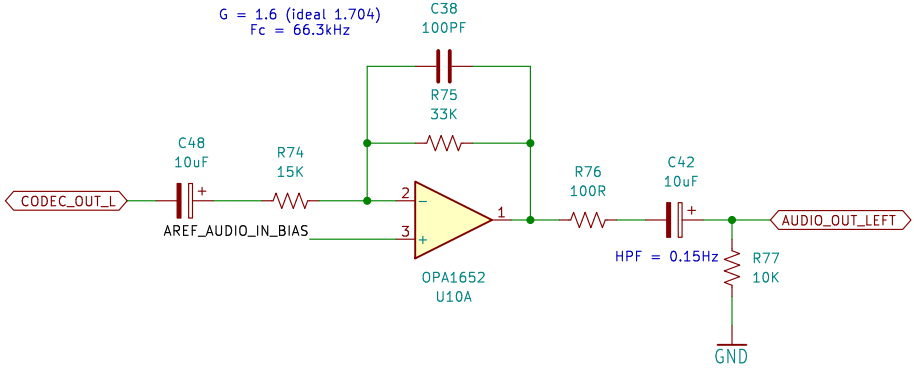
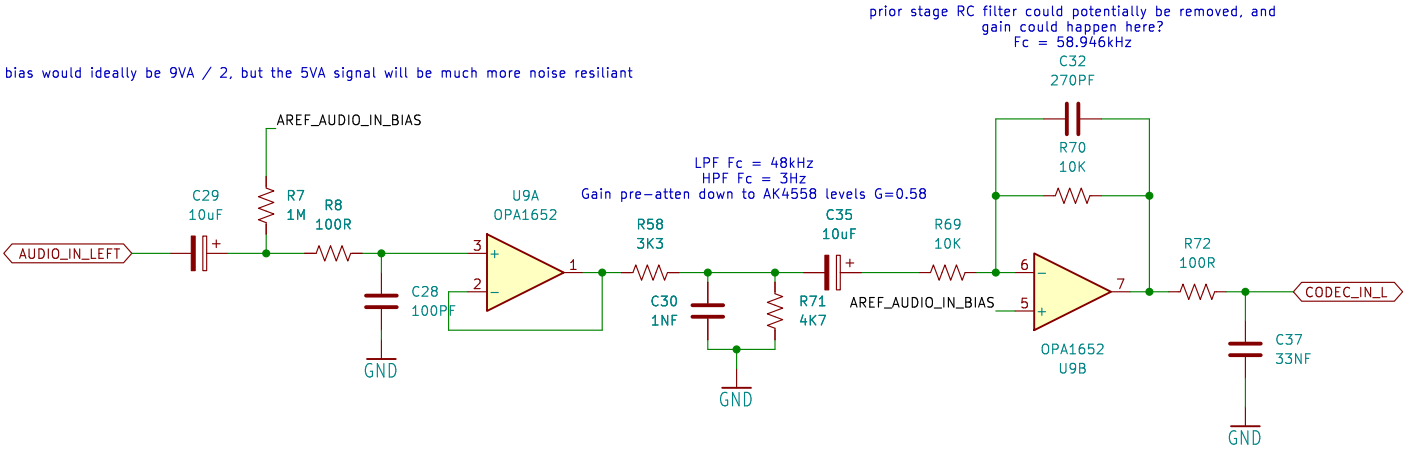


AK4558 In Range
0.8 * AVDD Vpp = 2.64Vpp @ AVDD=3.3V

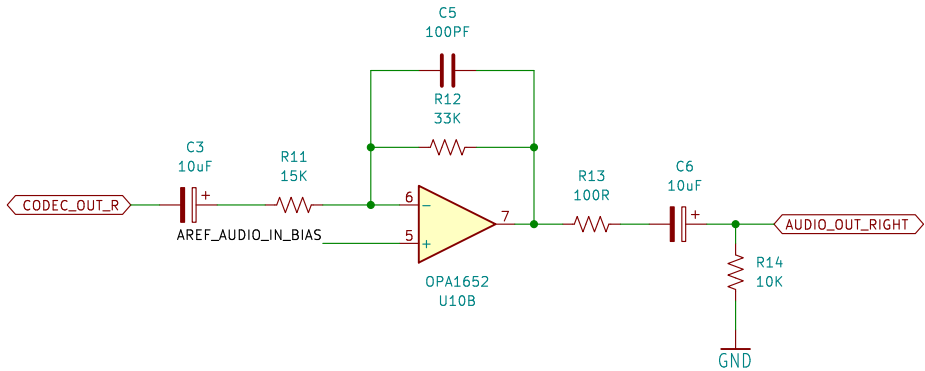
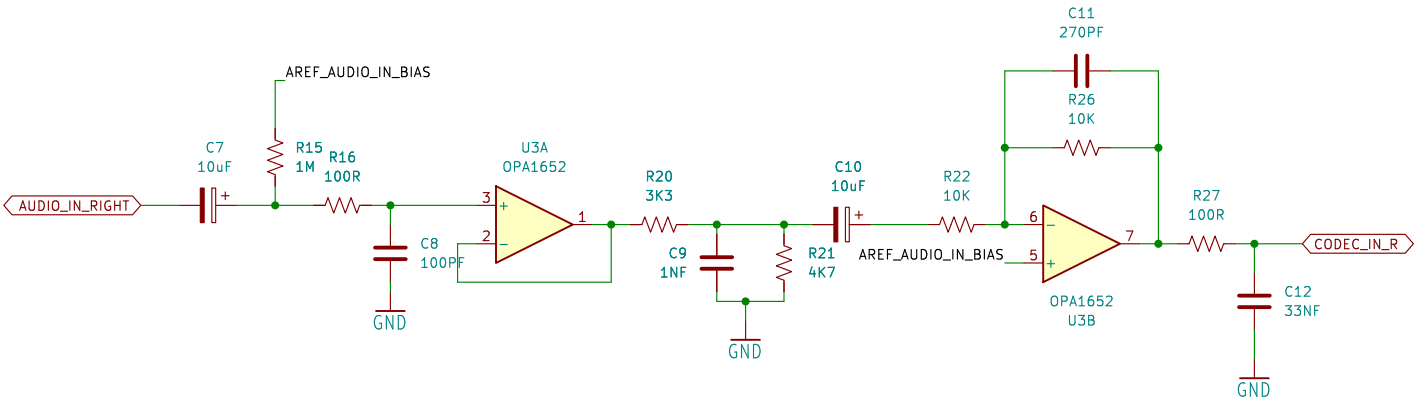
AK4558 Out Range
0.76 * AVDD Vpp = 2.508Vpp @ AVDD=3.3V

prior stage RC filter could potentially be removed, and gain could happen here?
 $f_c = 58.946\text{kHz}$

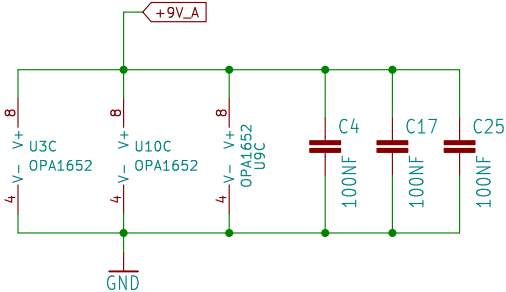
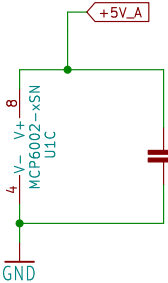
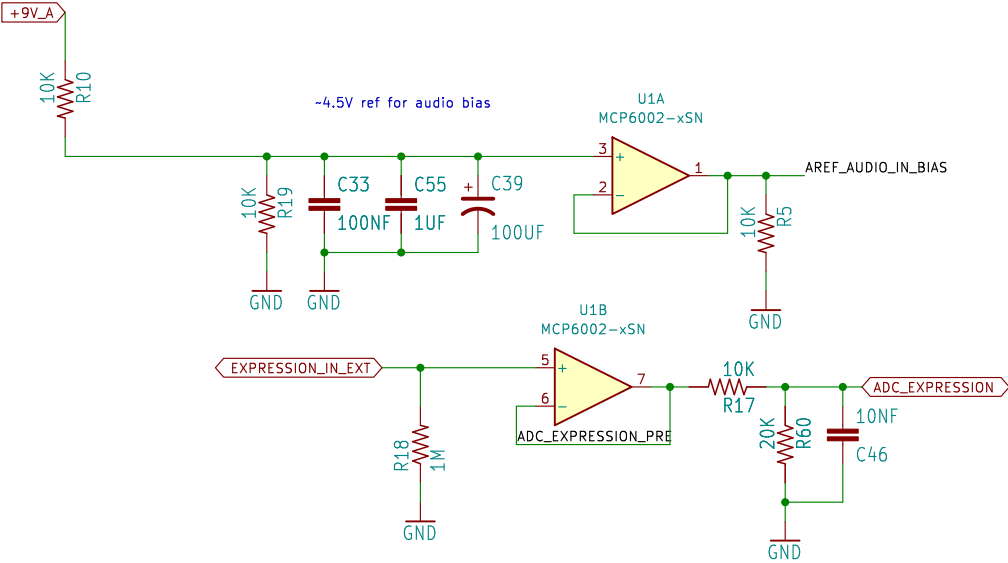
This bias would ideally be $9\text{V} / 2$, but the 5V_A signal will be much more noise resilient



--- Identical circuits for right audio channel ---



Expression In + VCOM Buffer



Power

